L Number	Hits	Search Text	DB	Time stamp
1 ·	490852	program\$5 ad2j logic adj2 device	USPAT	2004/09/29 00:35
2	3664	pld	USPAT	2004/09/29 00:35
3	491413	(program\$5 ad2; logic adj2 device) or pld	USPAT	2004/09/29 00:35
4	903553	memory or storage	USPAT	2004/09/29 00:36
5	8907	power adj2 state	USPAT	2004/09/29 00:37
6	149	first adj2 (power adj2 state)	USPAT	2004/09/29 00:39
7	44748	power adj2 (state or level)	USPAT	2004/09/29 00:39
8	1357	first adj2 (power adj2 (state or level))	USPAT	2004/09/29 00:40
9	1243	second adj2 (power adj2 (state or level))	USPAT	2004/09/29 00:40
10	47	(memory or storage) with (first adj2 (power	USPAT	2004/09/29 00:43
10		adj2 (state or level))) with (second adj2	ODINI	2004/03/23 00:43
		(power adj2 (state or level)))		
11	228	power adj2 (state of level)))	USPAT	2004/09/29 00:43
12	220	(power adj2 select\$3 adj2 signal) and	USPAT	2004/09/29 00:43
12	Ū	((memory or storage) with (first adj2 (power	OSFAI	2004/03/23 00:43
		adj2 (state or level))) with (second adj2		
	•	(power adj2 (state or level))))		
13	0	(power adj2 (state of level))) and	US-PGPUB;	2004/09/29 00:43
13	U	((memory or storage) with (first adj2 (power	EPO; JPO;	2004/03/23 00:43
		adj2 (state or level))) with (second adj2	DERWENT;	
		(power adj2 (state or level))))	IBM TDB	
15	0		_	2004/00/20 00:44
12	U	((memory or storage) with (first adj2 (power adj2 (state or level))) with (second adj2	US-PGPUB;	2004/09/29 00:44
			EPO; JPO;	
		(power adj2 (state or level)))) and	DERWENT;	
1.0	100040	((program\$5 ad2j logic adj2 device) or pld)	IBM_TDB	2004/00/00 00 40
18	199242	control adj2 circuit	USPAT	2004/09/29 00:48
19	1008634	rate	USPAT	2004/09/29 00:48
1	15864	first adj2 rate	USPAT	2004/09/29 00:48
21	17446	second adj2 rate	USPAT	2004/09/29 00:49
22	134	(control adj2 circuit) with (first adj2	USPAT	2004/09/29 00:51
	•	rate) with (second adj2 rate)		0004/00/00 00 54
23	0	((00.0000 0.0)	USPAT	2004/09/29 00:51
		rate) with (second adj2 rate)) and ((memory		
		or storage) with (first adj2 (power adj2		
		(state or level))) with (second adj2 (power		
		adj2 (state or level)))) and ((program\$5		
0.4	^	ad2j logic adj2 device) or pld))		
24	U	((control adj2 circuit) with (first adj2	US-PGPUB;	2004/09/29 00:51
		rate) with (second adj2 rate)) and ((memory	EPO; JPO;	
		or storage) with (first adj2 (power adj2	DERWENT;	
		(state or level))) with (second adj2 (power	IBM_TDB	
		adj2 (state or level)))) and ((program\$5		
		ad2j logic adj2 device) or pld))		
14	32	((memory or storage) with (first adj2 (power	USPAT	2004/09/29 00:51
		adj2 (state or level))) with (second adj2		
		(power adj2 (state or level)))) and		
L		((program\$5 ad2j logic adj2 device) or pld)		